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10/813,486	03/30/2004	Shervin Moloudi	15420US01	4921
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DAGLAWI, AMAR A				
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/813,486

Applicant(s)

MOLOUDI, SHERVIN

Examiner

AMAR DAGLAWI

Art Unit

2618

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 17 July 2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-40 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-40 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-946)
- 3) ☐ Information Disclosure Statement(s) (PTO/SF/ICE)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Response to Amendment

Claims 1-40 are pending in this application. Claims 1, 21, and 32 are amended to clarify the claim language. Claims 1, 21, and 32 are independent. The amendment has been entered.

Response to Arguments

1. Applicant's arguments with respect to claims 1-40 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-10, 14-21, 24-31 are rejected under 35 U.S.C. 102(b) as being anticipated by Yoshida (US 3,742,149).

4.

5. With respect to claim 1, the recitation "A method for reducing phase noise" has not been given patentable weight because it has not been held that a preamble is denied the effect of a limitation where the claim is drawn to a structure and the portion of the claim following the preamble is a self-contained description of the structure not depending for completeness upon the introductory clause. *Kropa v. Robie*, 88 USPQ 478 (CCPA 1951).

6. Yoshida teaches generating in a transmitter a signal at a particular frequency the signal being associated with a harmonic frequency signal disposed at a harmonic frequency (Fig.1, abstract, col.1, lines 5-53);
7. selecting in transmitter frequency content disposed in a region around the harmonic frequency and attenuating in said transmitter said selected frequency content disposed in said region around the harmonic frequency (Fig.1, abstract, col.1, lines 5-53).
8. With respect to claim 2, Yoshida further teaches associating the signal with a second harmonic frequency signal disposed at a second harmonic frequency and selectively attenuating frequency content disposed in a second region around the second harmonic frequency (Fig.2, abstract, col.1, lines 5-53).
9. With respect to claim 3, Yoshida further teaches applying at least one non-linear operation to the signal and transmitting the applied signal (Fig.1, Fig.2, abstract, col.1, lines 5-53).
10. With respect to claim 4, Yoshida further teaches applying at least one non-linear operation to the signal comprises dividing the signal (col.2, lines 30-52).
11. With respect to claim 5, Yoshida further teaches applying at least one non-linear operation to the signal comprises mixing the signal with a reference signal (Fig.1, col.2, lines 3-52).
12. With respect to claim 6, Yoshida further teaches applying at least one non-linear operation to the signal comprises amplifying the signal (Fig.1, col.2, lines 3—52).

13. With respect to claim 7, Yoshida further teaches the signal is generated by at least one of a fixed frequency oscillator, a voltage controlled oscillator, and a current controlled oscillator (Fig.1).

14. With respect to claim 8, Yoshida further teaches the frequency content is selectively attenuated by at least one attenuating circuit (Fig.1, col.1, lines 5-67, col.2, lines 1-25).

15. With respect to claim 9, Yoshida further teaches the at least one attenuating circuit comprises at least one of an integrated component and a discrete component (Fig.1).

16. With respect to claim 10, Yoshida further teaches the at least one attenuating circuit comprises at least one harmonic trap (Fig.1, #7, col.1, lines 5-67).

17. With respect to claim 14, Yoshida further teaches the signal comprises a differential signal (Fig.1).

18. With respect to claim 15, Yoshida further teaches the signal comprises a quadrature (Fig.1).

19. With respect to claim 16, Yoshida further teaches the selective attenuating comprises cancelling frequency content disposed in the region around the harmonic frequency (Fig.1, col.1, lines 5-67, col.2, lines 1-25).

20. With respect to claim 17, Yoshida further teaches the cancelling frequency content disposed in the region around the harmonic frequency comprises cancelling frequency content disposed only at the harmonic frequency (Fig.1, Fig.2, col.1, lines 5-67, co.2, lines 1-25).

21. With respect to claim 18, Yoshida further teaches the selective attenuating comprises notching frequency content disposed in the region around the harmonic frequency (Fig.1, Fig.2, col.1, lines 5-67, col.2, lines 1-25).
22. With respect to claim 19, Yoshida further teaches the notching frequency content comprises notching frequency content disposed only at the harmonic frequency (Fig.1, fig.2, col.1, lines 5-67, col.2, lines 1-25).
23. With respect to claim 20, Yoshida further teaches the selective attenuating comprises bandstopping frequency content disposed in the region around the harmonic frequency (Fig.1, fig.2, col.1, lines 5-67, col.2, lines 1-25).

With respect to claim 21, Yoshida teaches a signal generator in a transmitter, said signal generator generates a signal at a particular frequency, the signal being associated with a harmonic frequency signal disposed at a harmonic frequency (Fig.1, col.1, lines 5-67, col.2, lines 1-25); and

an attenuating circuit in said transmitter, that said attenuating circuit selects frequency content disposed in a region around the harmonic frequency and attenuates said selected frequency content disposed in said region around the harmonic frequency (Fig.1, Fig.2, #7, col.1, lines 5-67, col.2, lines 1-25).

With respect to claim 24, Yoshida further teaches a non linear operation circuit that applies at least one non-linear operation to the signal to obtain an outgoing signal and a transmitting circuit for transmitting the outgoing signal (Fig.1, Fig.2, #7, col.1, lines 5-67, col.2, lines 1-25).

With respect to claim 25, Yoshida further teaches the transmitting circuit comprises an antenna (Fig.1, col.2, lines 3-52).

24. With respect to claim 26, Yoshida further teaches non-linear operation circuit comprises a divider that divides the signal (Fig.1, col.2, lines 3-52).

25. With respect to claim 27, Yoshida further teaches the non-linear operation circuit comprises a mixer that mixes the signal with a reference signal (Fig.1, col.2, lines 3-52).

26. With respect to claim 28, Yoshida further teaches the non-linear operation circuit comprises an amplifier that amplifies the signal (Fig.1, col.2, lines 3—52).

27. With respect to claim 29, Yoshida further teaches the signal generator comprises at least one of a fixed frequency oscillator, a voltage controlled oscillator and a current controlled oscillator (fig.1).

28. With respect to claim 30, Yoshida further teaches the attenuating circuit comprises at least one of an integrated component and a discrete component (Fig.1).

29. With respect to claim 31, Yoshida further teaches the attenuating circuit comprises at least one harmonic trap (Fig.1, #7, col.1, lines 5-67).

30.

31. ***Claim Rejections - 35 USC § 103***

32. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

33. The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

34. Claims 11, 12, 13, 22-23, 32-40 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yoshida (US 3,742,149) in view of Cairns (US 5,794,131).

35. With respect to claims 11, 12, 13, 22-23 Yoshida teaches all the limitations of claim 1 except for buffering the signal prior to selectively attenuating the frequency content and wherein the buffering is performed by a buffer and wherein the selective attenuating of the frequency content is performed within the buffer which is taught in related art by Cairns (See col.4, lines 1-36, Fig.3).

36. It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the transmitter of Yoshida with the buffer of Cairns so as to provide sufficient attenuation to the third harmonics at the transmitter.

37. With respect to claim 32, Yoshida the recitation "A system for reducing phase noise" has not been given patentable weight because it has not been held that a preamble is denied the effect of a limitation where the claim is drawn to a structure and the portion of the claim following the preamble is a self-contained description of the structure not depending for completeness upon the introductory clause. *Kropa v. Robie*, 88 USPQ 478 (CCPA 1951).

Yoshida teaches a signal generator in a transmitter, said signal generator generates a signal at a particular frequency, the signal being associated with a harmonic frequency signal disposed at a harmonic frequency (Fig.1, col.1, lines 5-67, col.2, lines 1-25); and

38. However, Yoshida fails to teach a buffer that buffers the signal, the buffer adapted to select frequency content disposed in a region around the harmonic frequency and attenuate said selected frequency content disposed in said region around the harmonic frequency which is taught in related art by Cairns (See col.4, lines 1-36, Fig.3).

39. It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the transmitter of Yoshida with the buffer of Cairns so as to provide sufficient attenuation to the third harmonics at the transmitter.

40. With respect to claim 33, Yoshida in view of Cairns further teaches the signal is a differential signal (Fig.1).

41. With respect to claim 34, Yoshida in view of Cairns further teaches the signal is a quadrature (Fig.1).

42. With respect to claim 35, Yoshida in view of Cairns further teaches the signal generator comprises a differential signal generator (Fig.1).

43. With respect to claim 36, Yoshida in view of Cairns further teaches the buffer comprises a differential pair of transistors, the differential pair of transistors being adapted to receive the signal (Fig.3 of Cairns).

44. With respect to claim 37, Yoshida in view of Cairns further teaches the buffer comprises a harmonic trap the harmonic trap being adapted to attenuate the frequency content disposed in the region around the harmonic frequency (Cairns, Fig.3, col.4, lines 1-36).

45. With respect to claim 38, Yoshida in view of Cairns further teaches the harmonic trap is disposed across a differential output of the buffer (Cairns, Fig.3, col.4, lines 1-36).

With respect to claim 39, Yoshida in view of Cairns further teaches the buffer is adapted to band stop the frequency content disposed in the region around the harmonic frequency (Fig.1, Fig.2, #7, col.1, lines 5-67, col.2, lines 1-25).

46. With respect to claim 40, Yoshida in view of Cairns further teaches the buffer is adapted to notch the frequency content disposed only at approximately the harmonic frequency (Fig.1, Fig.2, #7, col.1, lines 5-67, col.2, lines 1-25).

Conclusion

47. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the

shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to AMAR DAGLAWI whose telephone number is (571)270-1221. The examiner can normally be reached on Monday- Friday (7:30 AM- 5:00 AM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, NGUYEN DUC can be reached on 571-272-7503. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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